

Electronic Acknowledgement Receipt

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Application Number:	10811835
International Application Number:	
Confirmation Number:	3051
Title of Invention:	Method of designing a circuit layout of a semiconductor device
First Named Inventor/Applicant Name:	Kazuaki Goto
Customer Number:	78198
Filer:	Donald R. Studebaker/Pamela Patrick
Filer Authorized By:	Donald R. Studebaker
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Payment was successfully received in RAM	\$1650
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part/.zip	Pages (if appl.)

1	Notice of Appeal Filed	notice_of_appeal.pdf	115994 7674e4c2dca7b6e14fea778547510b3084c 26417	no	1
Warnings:					
Information:					
2	Pre-Brief Conference request	pre-appeal_brief_request.pdf	111120 edb969da56af4f8f3df217e52aa854efd4c6d 02b	no	1
Warnings:					
Information:					
3	Pre-Brief Conference request	request_for_pre-appeal_brief_conference.pdf	34213 50a9722ee3792d7effb35a9e943e3e15eb1 0e02c	no	2
Warnings:					
Information:					
4	Fee Worksheet (PTO-06)	fee-info.pdf	32045 ff2a04501706b499e6ee37dd1e9ab254723 2d62d	no	2
Warnings:					
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Total Files Size (in bytes):				293372	
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